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DOCKET NO. SC13135TC



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Number of Pages (including this page)

Date: June 13, 2005  
To: H. Jey Tsai - 2812  
Location: United States Patent and Trademark Office  
Fax No.: (703) 872-9306  
From: Daniel D. Hill - 35,895  
Subject: 10/728,621- Yang Du et al

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## MESSAGE:

Enclosed herewith, please find a FORMAL AMENDMENT for filing in the below-identified application.

## ALL ITEMS MARKED WITH AN "X" ARE INCLUDED:

1.	x	1 page Facsimile Cover Sheet
2.	x	8 page Amendment

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EXAMINER: H. Jey Tsai  
GROUP ART UNIT: 2812  
SERIAL NO.: 10/728,621  
FILED: DECEMBER 5, 2003  
INVENTOR: YANG DU ET AL

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## UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT(S) Yang Du et al GROUP ART UNIT: 2812  
APPLN. NO.: 10/728,621 EXAMINER: H. Jey Tsai  
FILED: December 5, 2003  
TITLE: METHOD AND CIRCUIT FOR MULTIPLYING SIGNALS WITH A  
TRANSISTOR HAVING MORE THAN ONE INDEPENDENT GATE  
STRUCTURE

Certificate of Transmission under 37 CFR 1.8

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AMENDMENT

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Responsive to the Office Action dated March 23, 2005, and Examiner's comments with regard thereto, please enter the following amendments in the above-entitled application, without prejudice or disclaimer.

If Applicant has overlooked any additional fees, or if any overpayment has been made, the Commissioner is hereby authorized to credit or debit Deposit Account 503079.

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